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**From** Gerald P. Parsons  
**Total pages** 9  
**Subject** Application No.: 10/680,665  
**Your Ref.** Conf. No.: 5425  
**Our Ref.** SNDK.294US0

**MAIL STOP OFFICE OF INITIAL PATENT EXAMINATION  
COMMISSIONER FOR PATENTS  
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**Applicants:** George Matamis et al.  
**Title:** Bitline Direction Shielding to Avoid Cross  
Coupling Between Adjacent Cells for NAND Flash  
Memory  
**Application No.:** 10/680,665  
**Filing Date:** October 6, 2003  
**Conf. No.:** 5425  
**Atty Docket No.:** SNDK.294US0

**ATTACHED:**

1. Request for Corrected Filing Receipt (2 pages)
2. Copy of Declaration for Patent Application (4 pages)
3. Copy of return receipt postcard (1 page)
4. Marked up copy of Filing Receipt (2 pages)

PAG/meb

**Due Date:** n/a  
**Transmission Date:** June 3, 2005

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Applicants: George Matamis et al.  
Title: Bitline Direction Shielding to Avoid Cross Coupling Between Adjacent Cells for NAND Flash Memory  
Application No.: 10/680,665 Filing Date: October 6, 2003  
Examiner: Unknown Group Art Unit: 2818  
Docket No.: SNDK.294US0 Conf. No.: 5425

**Certificate of Transmission Under 37 CFR 1.8**

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office on June 3, 2005

Mary Z. Bugajin  
Signature

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**REQUEST FOR CORRECTED FILING RECEIPT**

Dear Sir:

Applicants hereby request the Commissioner For Patents to correct the Official Filing

Receipt for the above-identified patent application as follows:

- ☐ 1. Correct the Filing Date
- ☐ 2. Correct Inventor Name
- ☐ 3. Correct Title
- ☐ 4. Correct information referencing related case
- ☐ 5. Correct Attorney Docket Number
- ☒ 6. Other: Inventor Address

This Request is submitted for the following reasons:

Attorney Docket No.: SNDK.294US0

Application No.: 10/680,665

- 1 -

On the Filing Receipt the inventor George Matamis' address is shown as "Residence Not Provided". The address should be Nagoya, Aichi, as shown in the attached copy of the Declaration for Patent Application, which was filed with the application. Also attached is a copy of the return receipt postcard.

Attached is a copy of the Filing Receipt having the above errors and the changes noted thereon. Therefore, it is respectfully requested that a corrected Filing Receipt be issued. If this action does not lead to issuance of a corrected Official Filing Receipt as requested, please contact the undersigned at (415) 318-1163.

Respectfully submitted,



Gerald P. Parsons  
Reg. No. 24,486

June 3, 2005

Date

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(415) 693-0194 (fax)

Attorney Docket No.: SNDK.294US0

Application No.: 10/680,665

- 2 -

**MAIL STOP PATENT APPLICATION  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450**

Applicants: G. Matamis et al.  
Title: Bidline Direction Shielding to Avoid Cross Coupling Between Adjacent Cells for NAND Flash Memory  
Application No.: Applied for  
Filing Date: October 6, 2003  
Any Docket No.: SSDK 294/US0

**ENCLOSED:**

1. This Return Receipt Postcard
2. Transmittal Letter (1 page x 2)
3. Specification (9 pages)
4. Claims (4 pages)
5. Abstract (1 page)
6. Drawings (5 sheets)
7. Executed Declaration (In counterparts - 4 pages)
8. Executed Power of Attorney (2 pages)
9. Check in the amount of \$1026.00

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Express Mailing Date: October 6, 2003

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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/680,665	10/06/2003	2818	1050	SNDK.294US0	5	26	5

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CONFIRMATION NO. 5426

## FILING RECEIPT



\*OC000000011606513\*

Date Mailed: 01/02/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

## Applicant(s)

Nagoya, Aichi

George Matamis, ~~Residence Not Provided~~,  
 Tuan Pham, San Jose, CA;  
 Henry Chien, San Jose, CA,  
 Hao Fang, Cupertino, CA;

50K0361.00005  
 SNOK.294US0

GAP/PAG

Domestic Priority data as claimed by applicant

Foreign Applications

If Required, Foreign Filing License Granted: 12/30/2003

Projected Publication Date: 04/07/2005

Non-Publication Request: No

Early Publication Request: No

## Title

Bitline direction shielding to avoid cross coupling between adjacent cells for NAND flash memory

Preliminary Class

365

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Title 37, Code of Federal Regulations, 5.11 & 5.15**

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